

International Application No.: PCT/JP2005/000605

U.S. Patent Application No.: Unknown

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Page 3 of 11

IN THE ABSTRACT:

Please replace the Abstract of the Disclosure originally filed with the above-identified patent application with the following new Abstract of the Disclosure:

ABSTRACT OF THE DISCLOSURE

An overcurrent detection circuit that minimizes fluctuations in the overcurrent detection level when the input supply voltage or temperature fluctuates includes a monitor transistor having a control terminal and an output terminal which are connected to a control terminal and an output terminal respectively of an output transistor, an output current detection transistor having a control terminal to which a detection bias voltage is input, and an output terminal which is connected to an input terminal of the monitor transistor, a constant current source that generates a reference current, a reference transistor having a control terminal to which the detection bias voltage is input and an output terminal to which the reference current flows to the constant current source, and a comparison circuit that outputs an overcurrent detection signal by comparing the voltage of the output terminal of the output current detection transistor and the voltage of the output terminal of the reference transistor.